

# CALL FOR PAPERS

## The Ninth IEEE International Symposium on Asynchronous Circuits and Systems

### Vancouver, 12-16 May 2003

ASYNC 2003 is the ninth in the series of annual symposia on self-timed and asynchronous circuit techniques, with special focus on state-of-the-art and innovative application domains where asynchronous techniques can help improve performance, power consumption, reliability, low EM radiation, or design time. Topics of current interest include, but are not necessarily limited to:

- mixed synchronous/asynchronous architectures, interfaces, and circuits
- high-speed and low-power design for logic, memories, and interconnect
- high-level design and synthesis of self-timed circuits
- formal methods for self-timing and performance analysis
- test, reliability, security and radiation tolerance
- design experiences with CAD tools for asynchronous design
- Globally-Asynchronous-Locally-Synchronous systems (GALS)

**WEBSITE:** <http://research.sun.com/async2003>

#### SUBMISSION INFORMATION:

Submission deadline: 23 Nov 2002  
Notification of acceptance: 23 Jan 2003  
Final version due: 28 Feb 2003  
Paper Format: Abstract of up to 150 words,  
10 pages or fewer, including figures,  
Single spaced, 10pt or larger font size, or IEEE format.

For electronic submission procedures, see the ASYNC 2003 web site. The proceedings will be published by the IEEE Computer Society Press. Queries about paper submissions may be sent to the program committee chairs.

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